Publications (as of September 17, 2020)

Books
7. G. De Micheli and L. Benini Networks on Chips Morgan Kaufmann, 2006

Book Chapters (fully refereed)


Journals (fully refereed)


Editorials (invited papers)


**Conference Proceedings (fully refereed and with unlimited distribution)**


616. K. Kang, L. Benini and G. De Micheli, “A High-throughput and Low-Latency Interconnection Network for Multi-Core Clusters with 3-D Stacked L2 Tightly-Coupled Data Memory”. IFIP/IEEE International Conference on Very Large Scale Integration (VLSI-SoC), Santa Cruz, California, USA, 2012.


666. X. Tang, P.-E. Gaillardon and G. De Micheli. “Pattern-Based FPGA Logic Block and Clustering Algorithm.” 24th International Conference on Field Programmable Logic and Applications (FPL), Munich, Germany, 2014.


788. F. Criscuolo; M.Galfione; S. Carrara; G. De Micheli , "All-solid-state Reference Electrodes for analytical applications" 8th IEEE International Workshop on Advances in Sensors and Interfaces (IWASI), Otranto, Italy, June 2019


790. I. Ny Hanitra, F. Criscuolo, S. Carrara, G. De Micheli, " Multi-Target Electrolyte Sensing Front-End for Wearable Physical Monitoring", 15th Conference on Ph.D Research in Microelectronics and Electronics (PRIME), Otranto, Italy, June 2019


Conference Proceedings Abstracts (fully refereed)


**Other Publications (fully refereed and with limited distribution)**


**Other Publications (technical reports)**


