

## Publications (as of April 25, 2024)

### Books

1. G. De Micheli, A. Sangiovanni-Vincentelli and P. Antognetti, Editors, *Design Systems for VLSI Circuits: Logic Synthesis and Silicon Compilation*, Martinus Nijhoff, 1987.
2. D. Ku and G. De Micheli, *High-Level Synthesis of ASICs Under Timing and Synchronization Constraints*, Kluwer Academic Publishers, 1992.
3. G. De Micheli, *Synthesis and Optimization of Digital Circuits*, McGraw-Hill, 1994.
4. G. De Micheli and M. Sami, Editors, *Hardware/Software Co-design*, Kluwer Academic Publishers, 1996.
5. L. Benini and G. De Micheli, *Dynamic Power Management of Circuits and Systems: Design Techniques and CAD Tools*, Kluwer Academic Publishers, 1998.
6. G. De Micheli, R. Ernst and W. Wolf, *Readings in Hardware/Software Co-Design*, Morgan Kaufmann, 2001.
7. G. De Micheli and L. Benini *Networks on Chips* Morgan Kaufmann, 2006
8. G. De Micheli, Giovanni, S. Mir and R. Reis, Editors *VLSI-SoC: Research Trends in VLSI and Systems on Chip*, Springer 2008.
9. G. De Micheli, M. Gijs, Y. Leblebici and J. Vörös, Editors, *Nanosystems Design and Technology*, Springer, 2009.
10. A. Leblebici, P. Mayor, M. Rajman and G. De Micheli *Nano-Tera.ch - Engineering the Future of Systems for Health, Environment and Energy*. Springer, 2019.
11. C. Metzler, P.E. Gaillardon, G. De Micheli, C. Silva-Cardenas and R. Reis, Editors *VLSI-SOC: New Technology Enabler*, Springer, 2020

### Book Chapters (fully refereed)

12. G. De Micheli, M. Hofmann, A. R. Newton and A. Sangiovanni-Vincentelli, “A Design System for PLA-based Digital Circuits,” in A. Sangiovanni-Vincentelli, Editor, *Advances in Computer Engineering Design*, Jai Press, 1985, pp. 285-364.
13. G. De Micheli, “Design of Control Systems,” in G. De Micheli, A. Sangiovanni-Vincentelli and P. Antognetti, Editors, *Design Systems for VLSI Circuits: Logic Synthesis and Silicon Compilation*, Martinus Nijhoff, 1987, pp. 327-364 and *IBM Research Report*, No. RC 12062.
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17. D. Ku and G. De Micheli, “High-level Synthesis and Optimization Strategies in Hercules and Hebe,” in P. Michel and G. Saucier, Editors, *Logic and Architecture Synthesis*, North Holland, pp. 111-120, 1991. (Cross-listed as Conference Proceedings No. 400.)
18. D. Ku and G. De Micheli, “Synthesis of ASICs with Hercules and Hebe,” in R. Camposano and W. Wolf Editors, *Trends in High-Level Synthesis*, Kluwer, 1991, pp. 177-203 and *Stanford University Report*, CSL-TR-91-461, February 1991.
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23. G. De Micheli, "Cell-based logic optimization," in E. Boerger Editor *Architecture Design and Validation Methods*, Springer, pp.49-88, 2000.
24. L. Benini and G. De Micheli, "Logic Synthesis for Low Power," in S. Hassoun and T. Sasao, Editors, *Logic Synthesis and Verification*, Kluwer, pp. 197-223, 2002.
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41. G. De Micheli, "Electronic Systems for Health Management, in. S. Carrara and K. Iniewski Editors, *Handbook of Bioelectronics: Directly Interfacing Electronics and Biological Systems*, Part VIII, Chapter 42, p. 543-549, Cambridge Press, 2015.
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48. S.-Y. Lee, H. Riener, and G. De Micheli, "External Don't Cares in Logic Synthesis", in R. Drechsler and S. Huhn Editors, *Advanced Boolean Techniques* Springer 2023

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